



PTO-1449 (Modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Attorney 2003-IP-09595 / Docket No.: HESI.105897	Serial Number: 10/628,781
	Applicant: Chris Chuter	
	Filing Date: July 28, 2003	Group:

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	ISSUE DATE	INVENTOR	CLASS	SUBCLASS	FILING DATE
<i>Rem</i> <i>7/18/07</i> <i>AL.</i>	Pub. No. 2002/0172401 (09/936,780)	<i>11/2002</i>	Lees, Jack, et al.	382	109	9/17/01

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

EXAMINER INITIAL	OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place Of Publication)
<i>AL.</i>	Mark J. Kilgard, "A Practical and Robust Bump-mapping Technique for Today's GPUs," July 5, 2000, pp. 1-39, NVIDIA Corporation, Santa Clara, CA
<i>AL.</i>	Jack Lees, "Constructing Faults from Seed Picks by Voxel Tracking," March 1999, pp. 338, 340, <i>The Leading Edge</i> .
EXAMINER <i>Art Cole</i>	DATE CONSIDERED <i>2/13/06</i>
EXAMINER: Initial citation if reference was considered. Draw line through citation if not in conformance to MPEP 609 and not considered. Include copy of this form with next communication to applicant.	